

FIG. 1 is a block diagram of a system 100 for transmitting and receiving data. The system 100 includes a transmitter 102 and a receiver 110. The transmitter 102 is connected to a front end 106, which is connected to an ADC 108. The front end 106 is also connected to a receiver 110. The receiver 110 is connected to a front end 112, which is connected to a DAC 114. The front end 112 is also connected to a receiver 110.

100

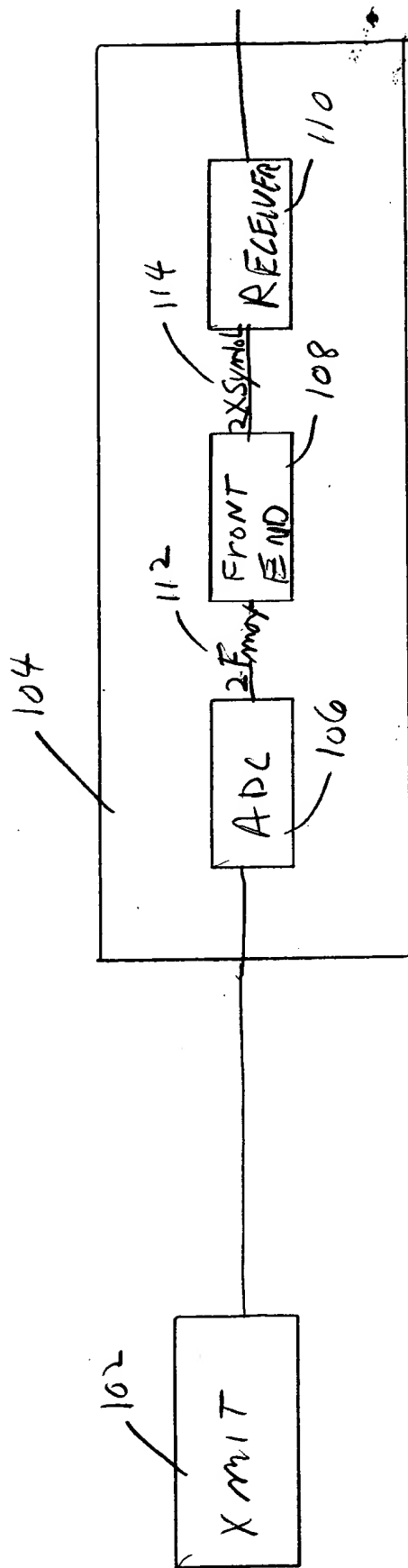


FIGURE 1

FIGURE 2A

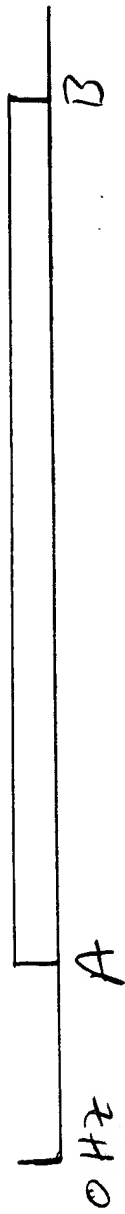


FIGURE 2B

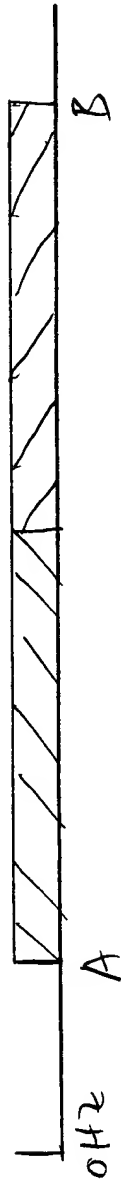


FIGURE 2C

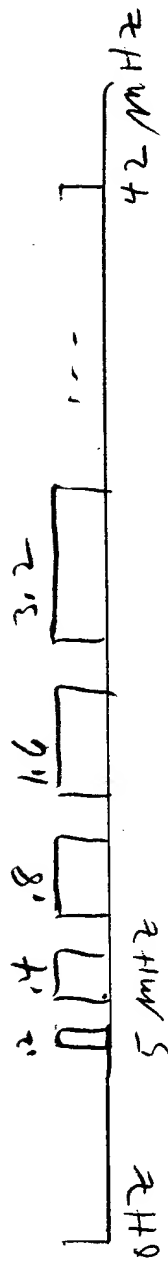


FIGURE 2D

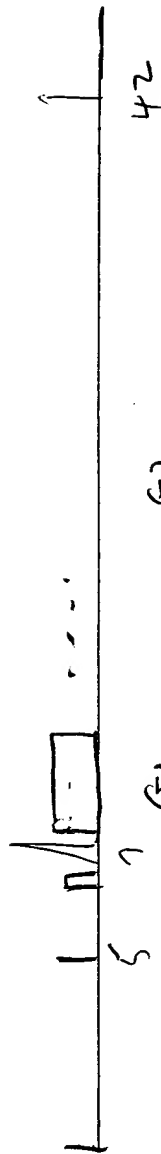


FIGURE 2E



100

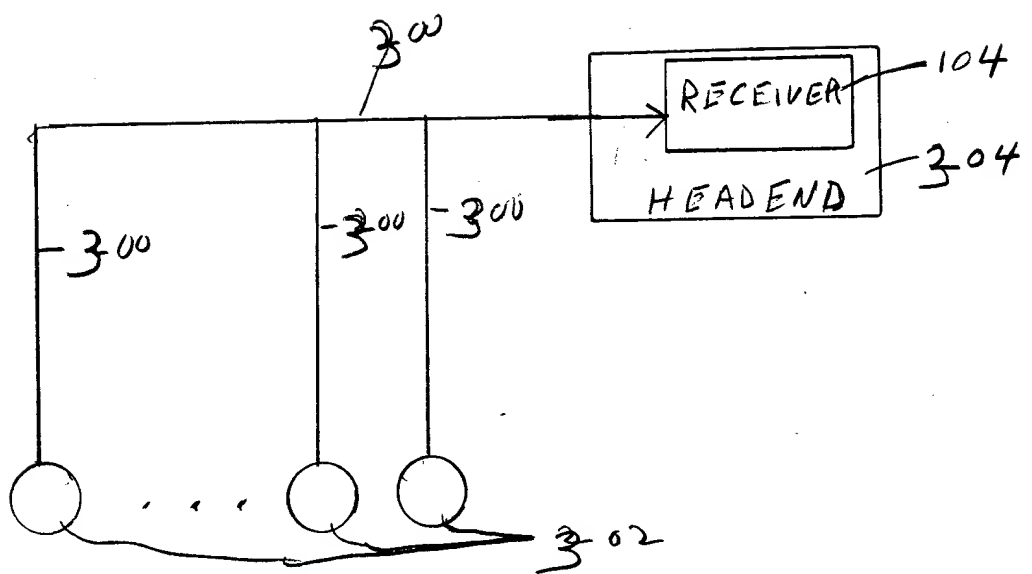
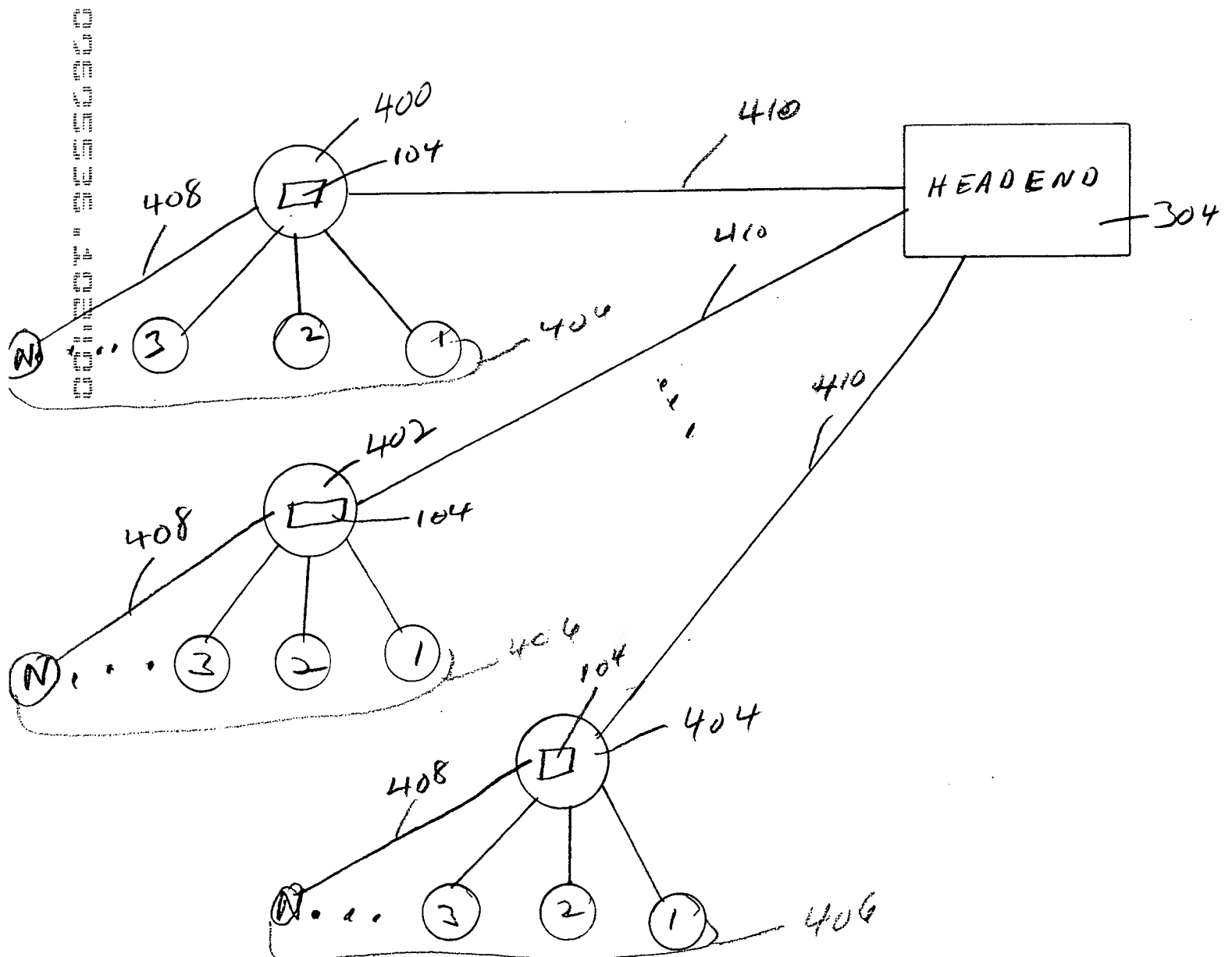


FIGURE 3

FIGURE 4



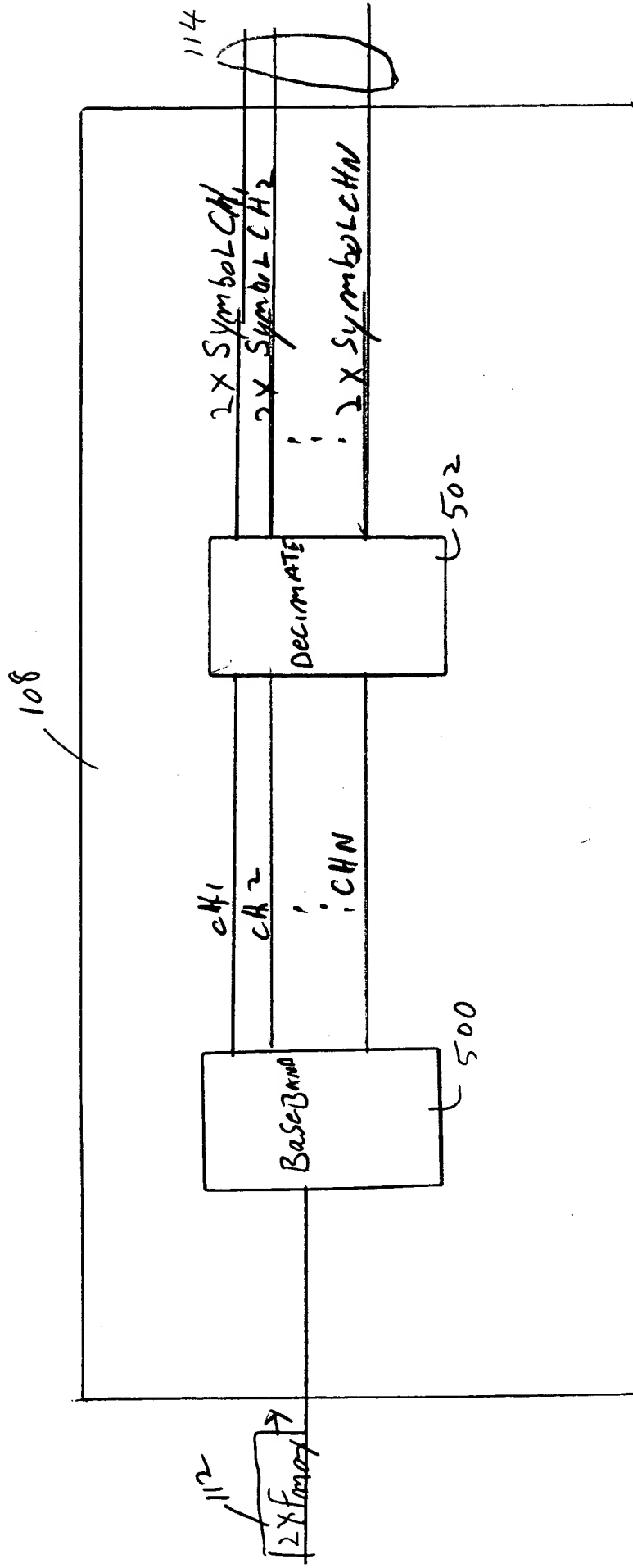


FIGURE 5

FIG. 10 is a block diagram of a system for processing multiple channels of data.

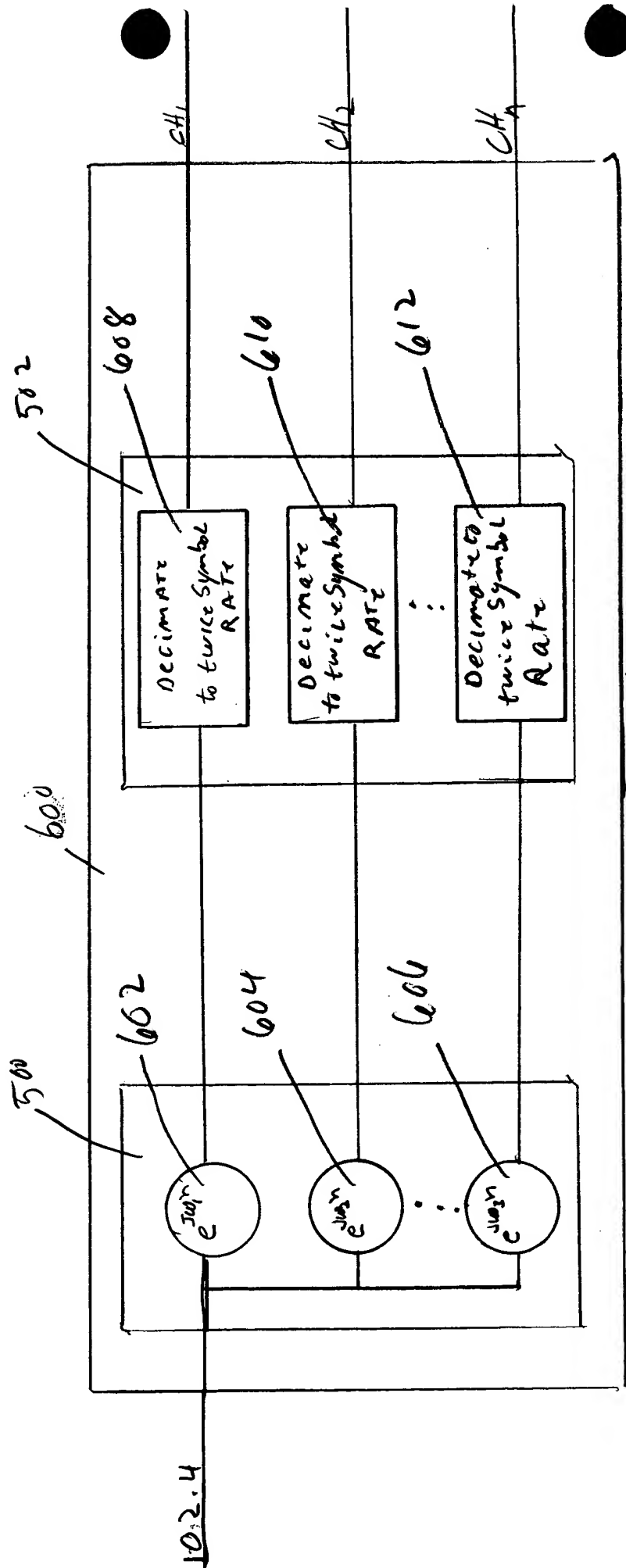
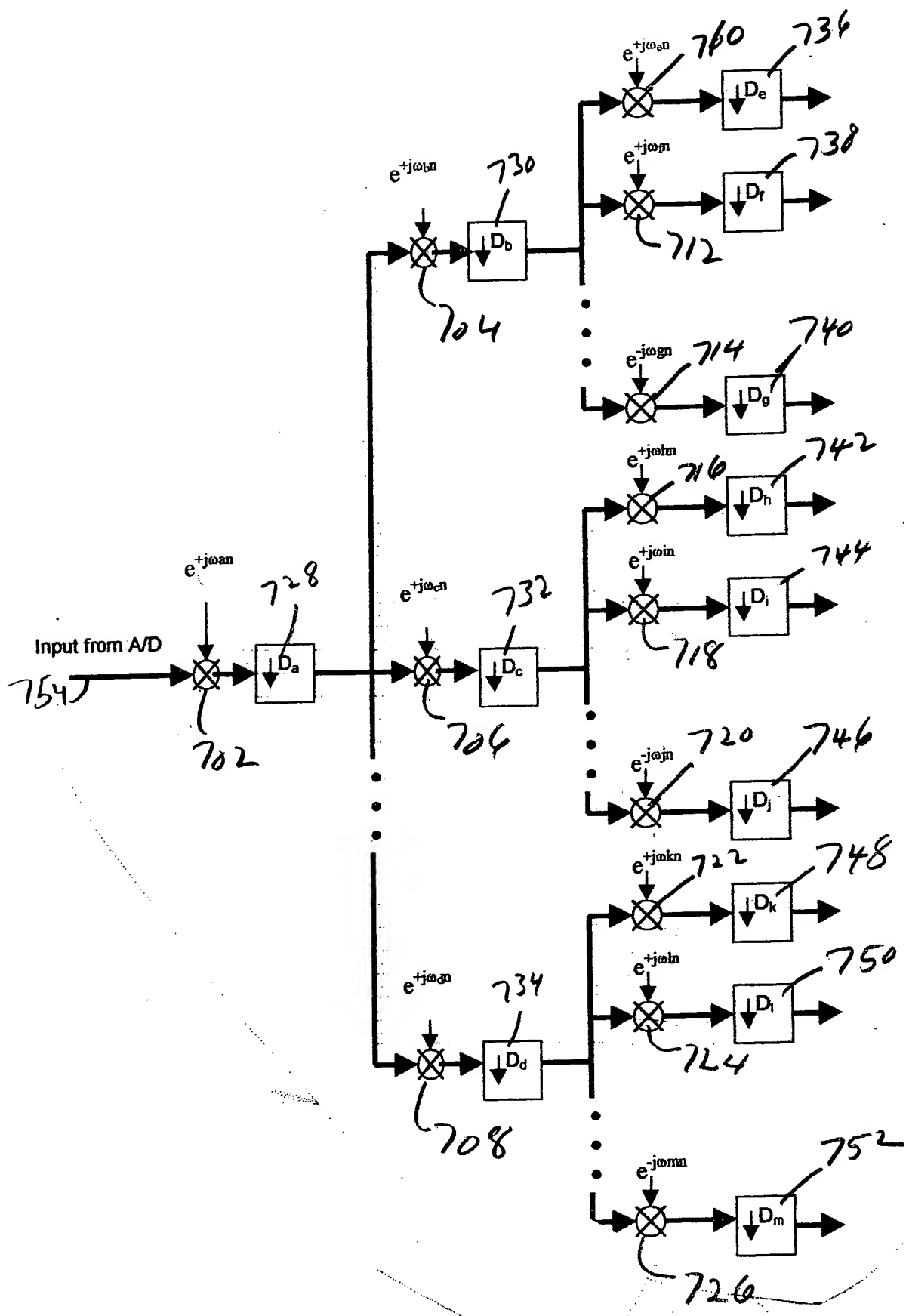
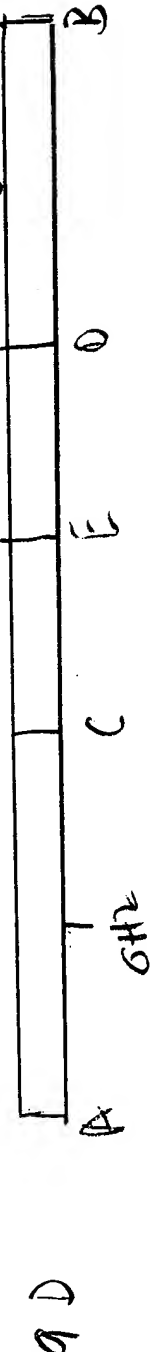
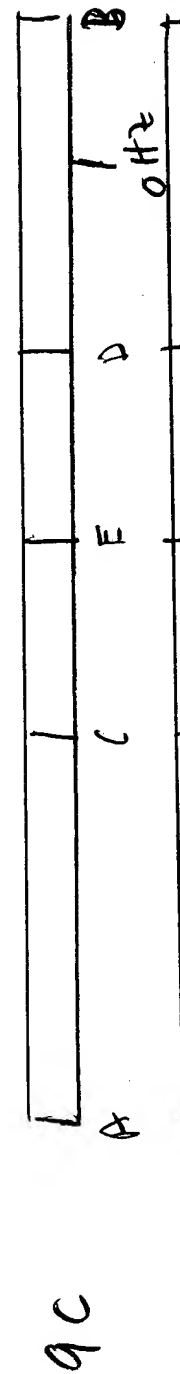
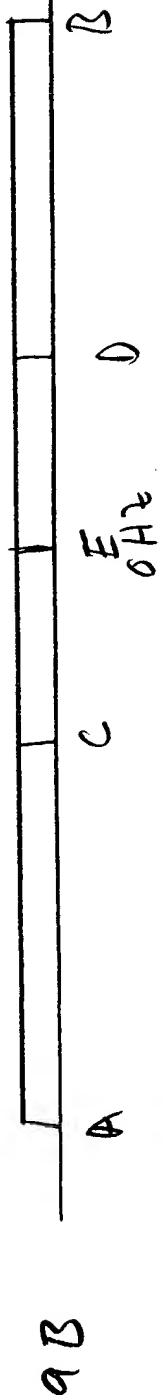
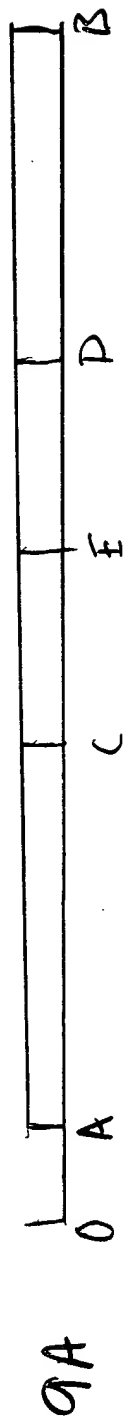
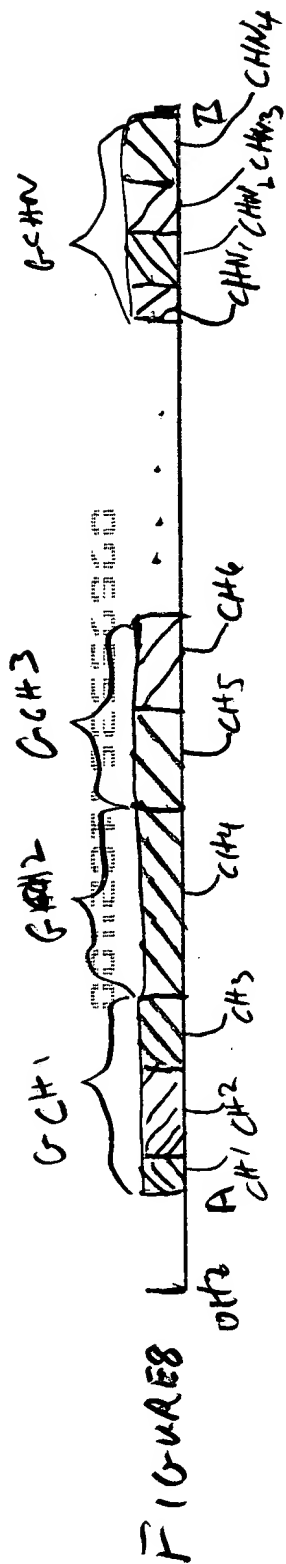


FIGURE 6

FIGURE 7





1000

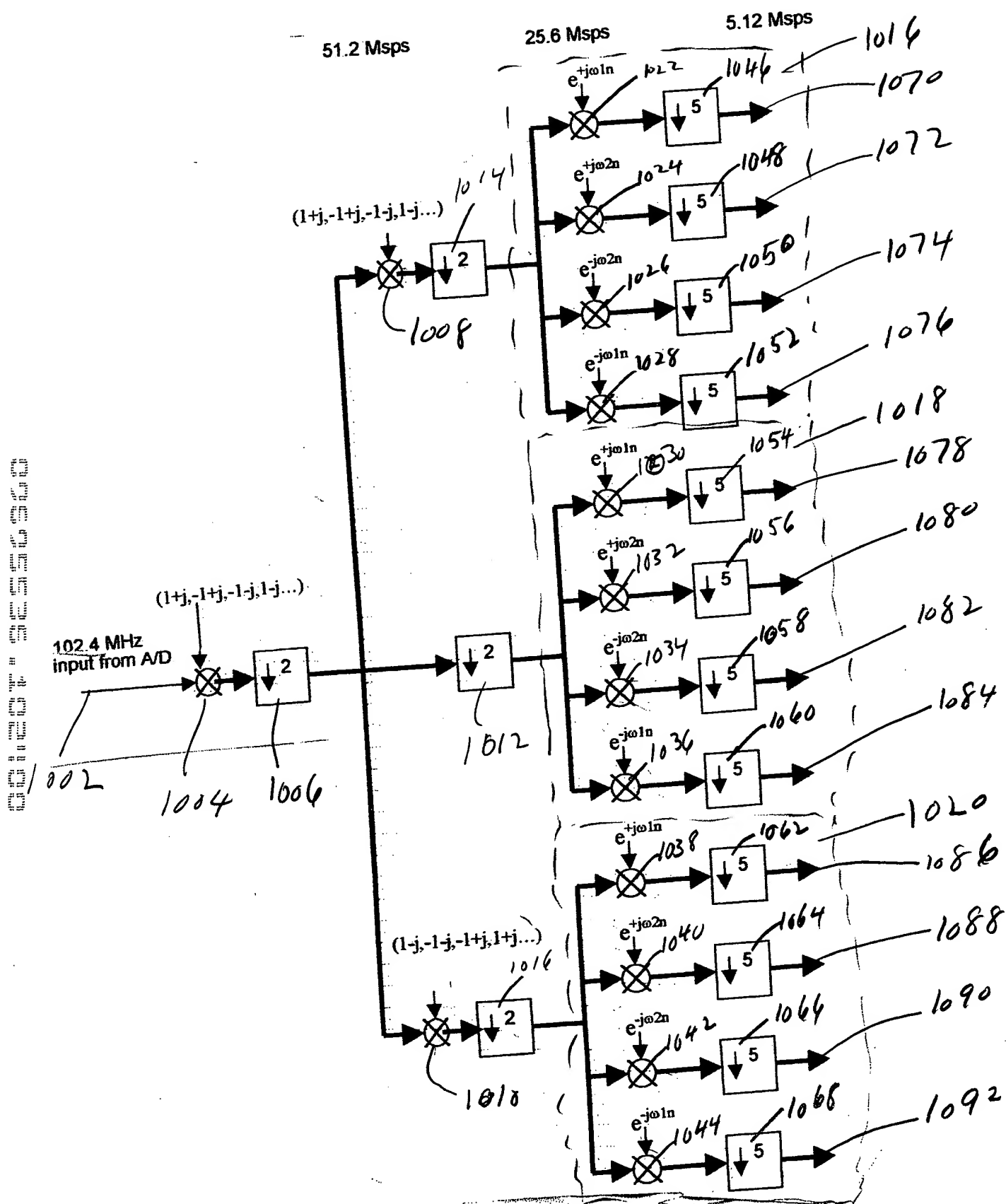


FIGURE 11

1100

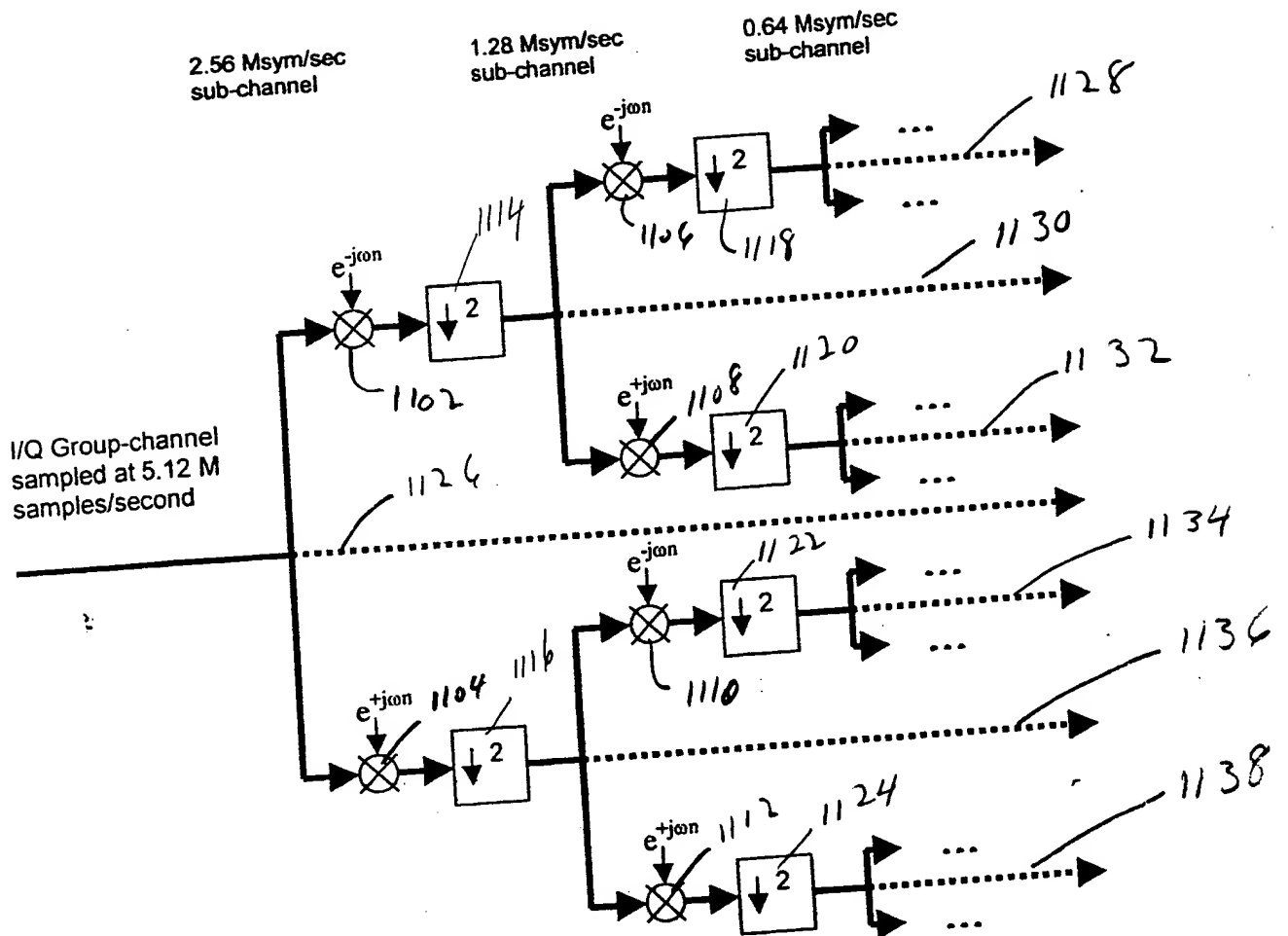
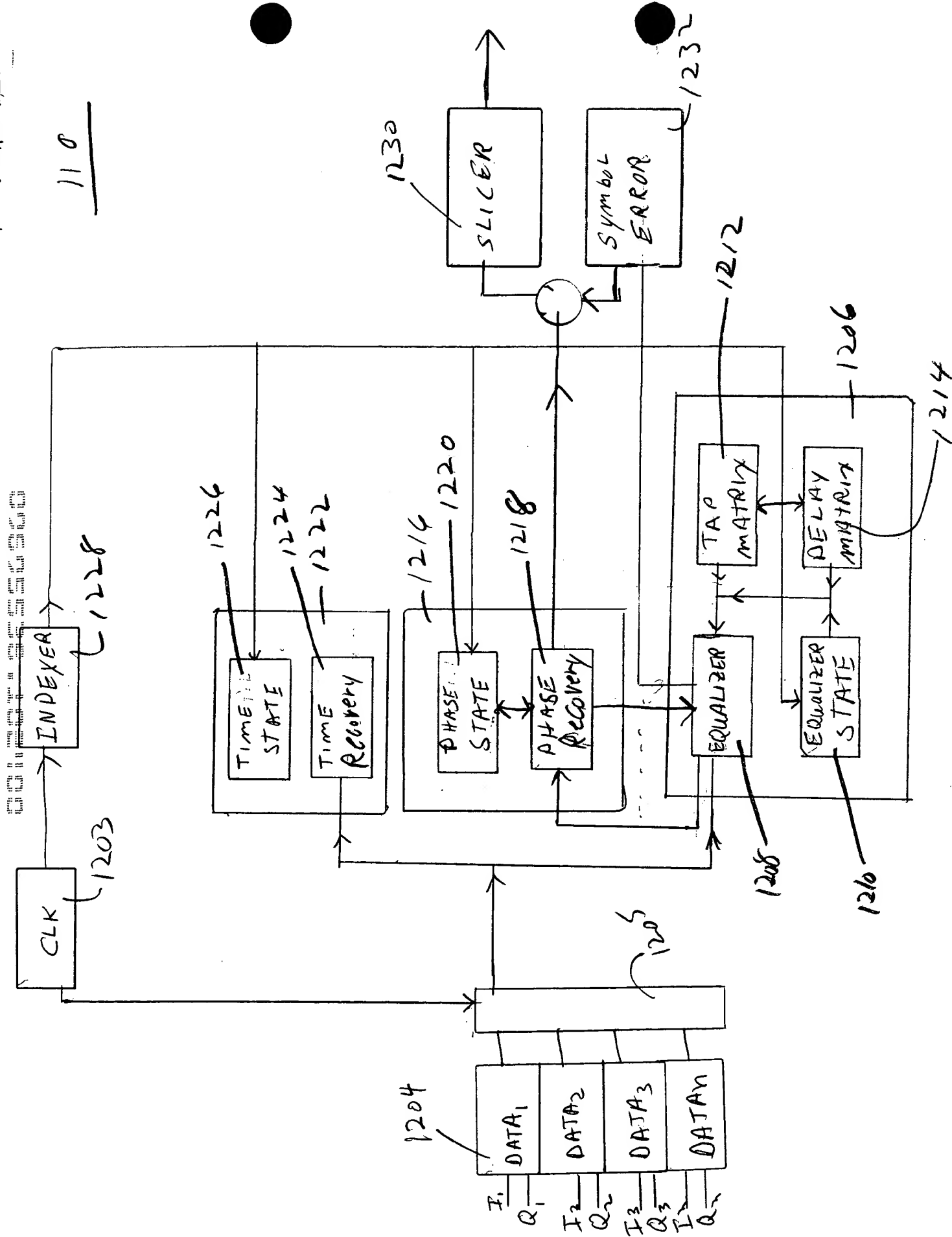


FIGURE 12

110



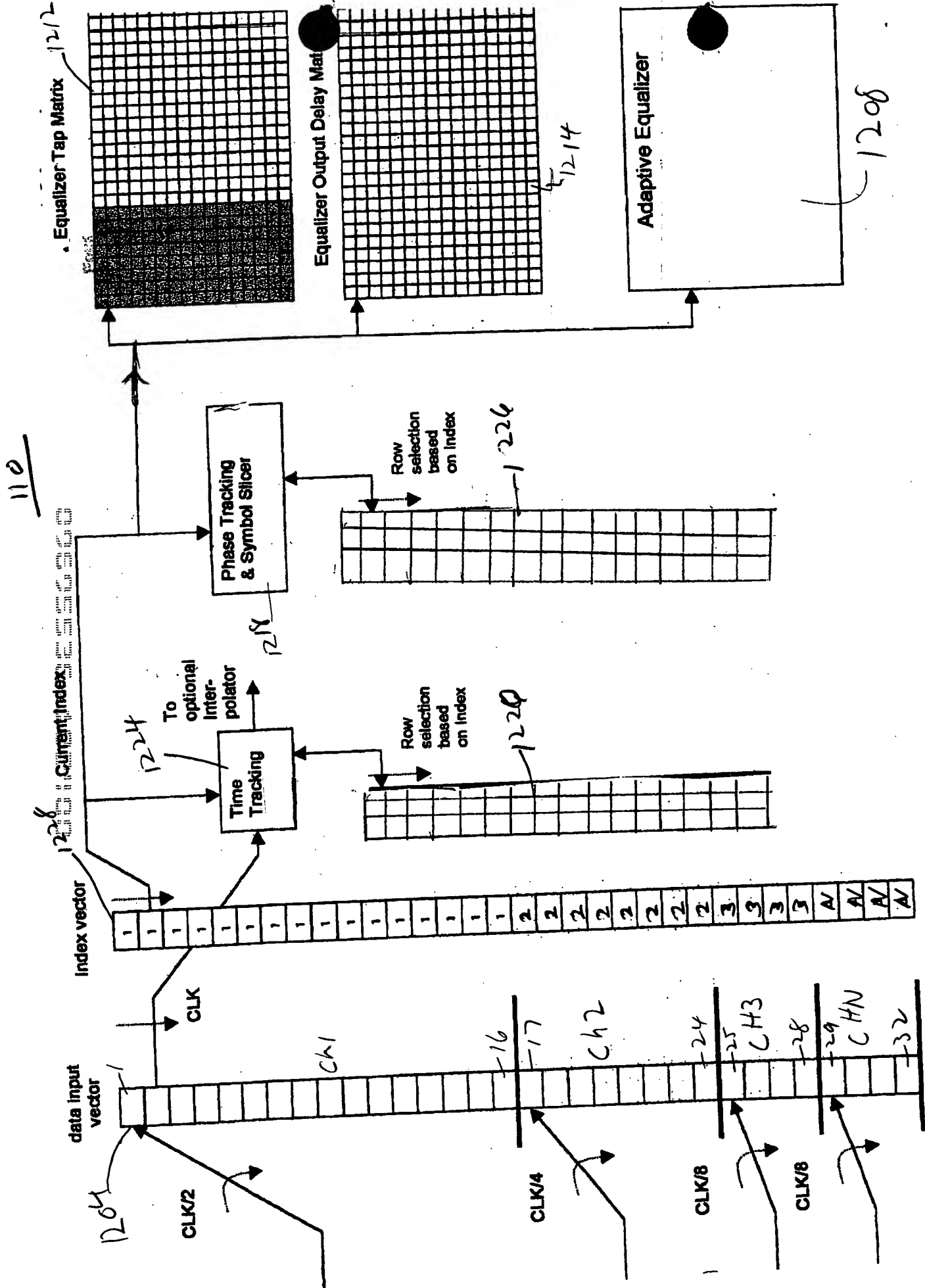


FIGURE 13